

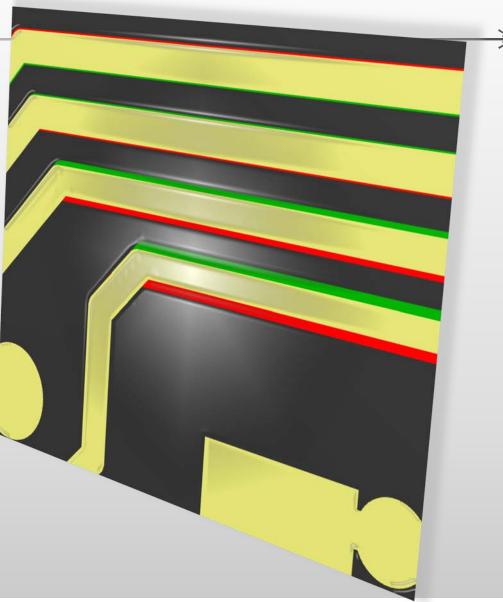
YELO Yield Enhancing Layout Optimizer

Yelo (Yield Enhancing Layout Optimizer) YELO-What is it?

Imagine you could...

- Improve board layouts in minutes
- Cut your CAM time by up to 30%
- Archive superior production yields
- Deliver more reliable PCBs
- Slash production cost on every batch

Well actually, with YELO now you can ...



What is it?



YELO – Yield-Enhancing Layout Optimizers

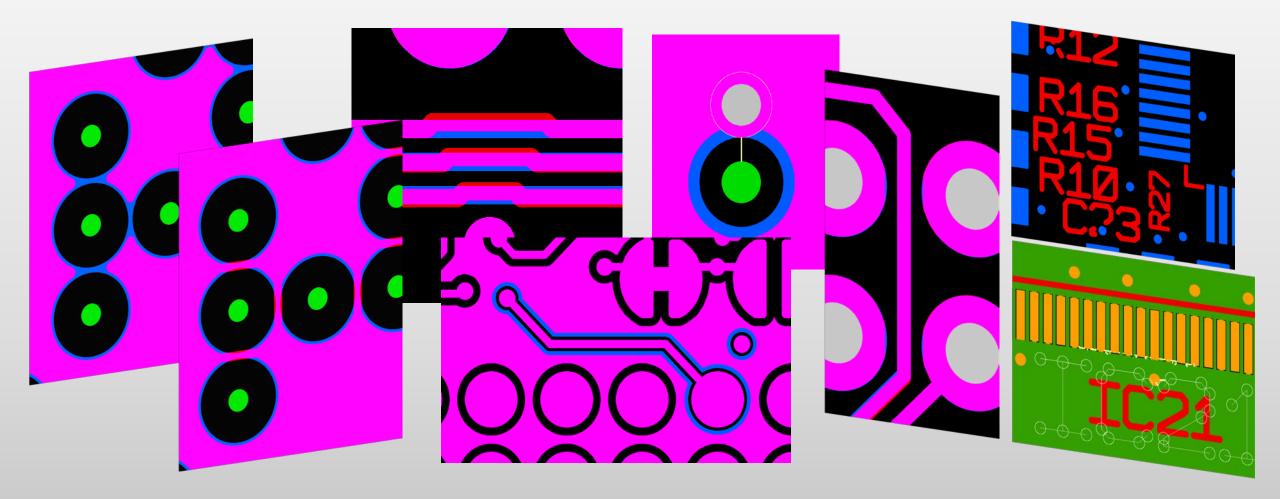
- Brand new suite of software modules for automated PCB layout optimization
- Optimizing layouts for superior production yields
- Highly automated, integrated and industrialized workflow
- With unparalleled throughput
- And uncompromising results

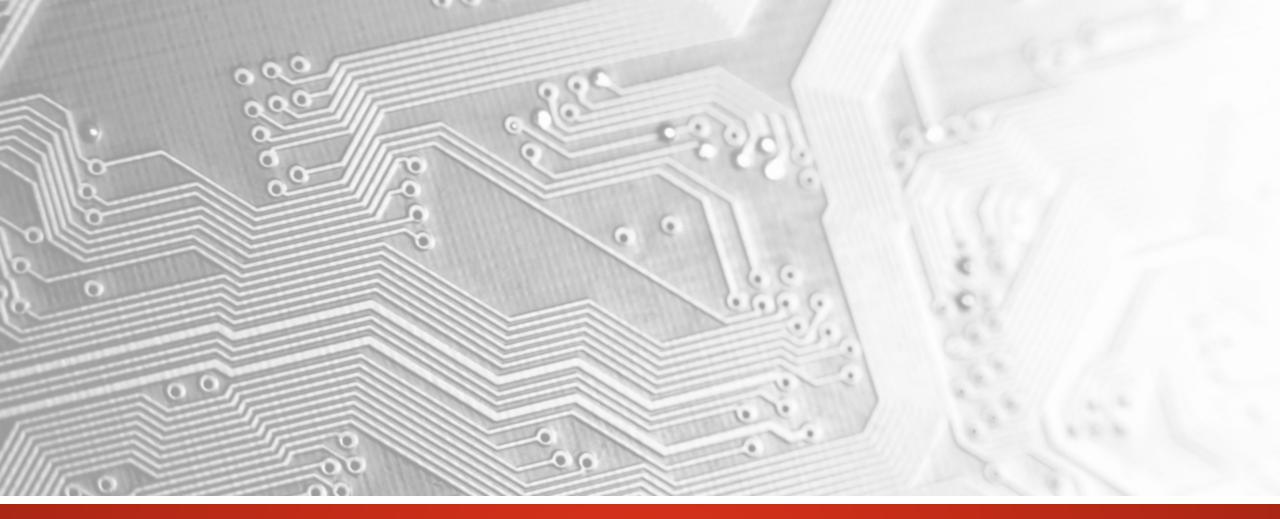
YELO consists of:

Copper Adjuster, Legend Adjuster



Find out what Copper Adjuster – Signal, Copper Adjuster – Plane and Legend Adjuster can do for you...



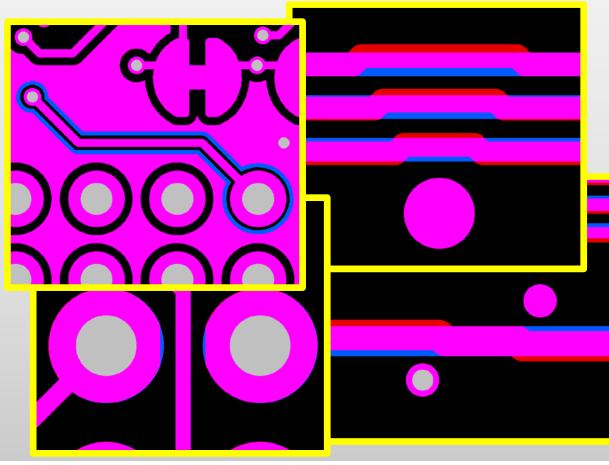


Copper Adjuster - Signal - Plane

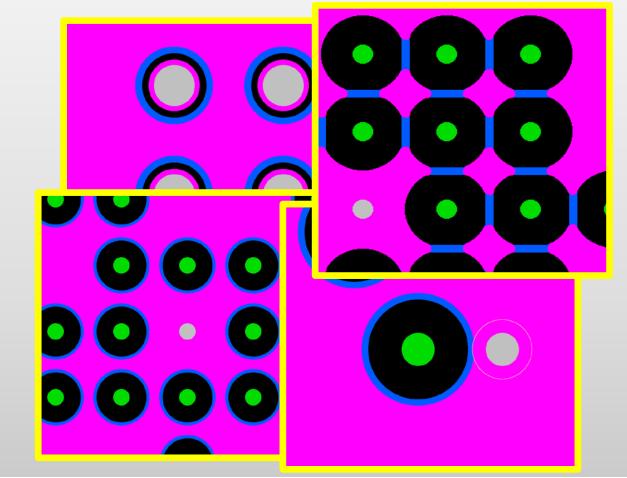
Copper Adjuster (CAJ)

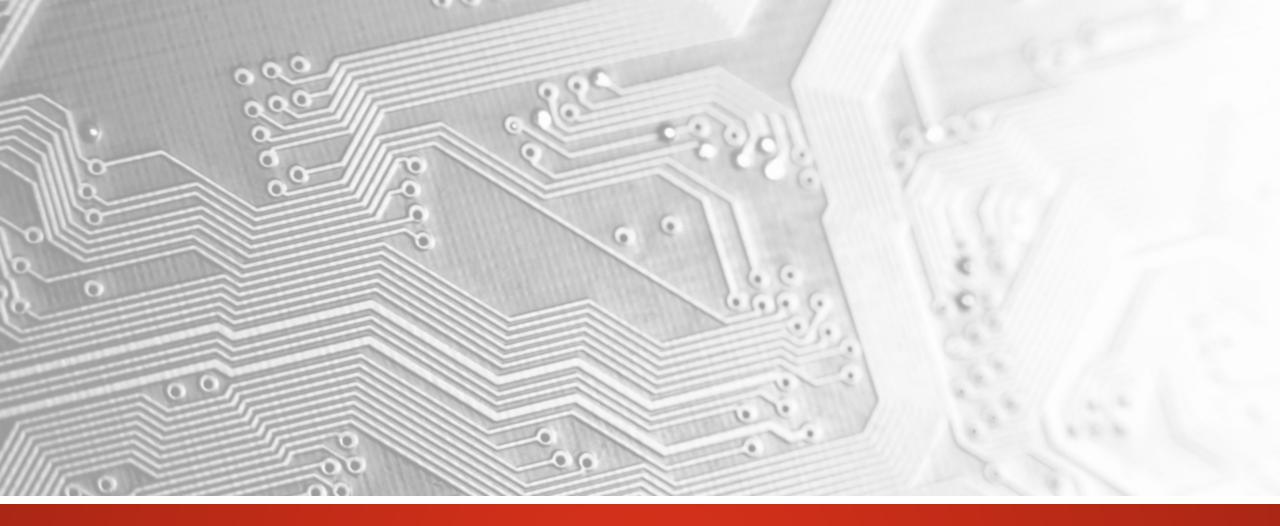
Copper Adjuster consists of 2 modules:

Copper Adjuster (CAJ) - Signal



Copper Adjuster (CAJ) - Plane

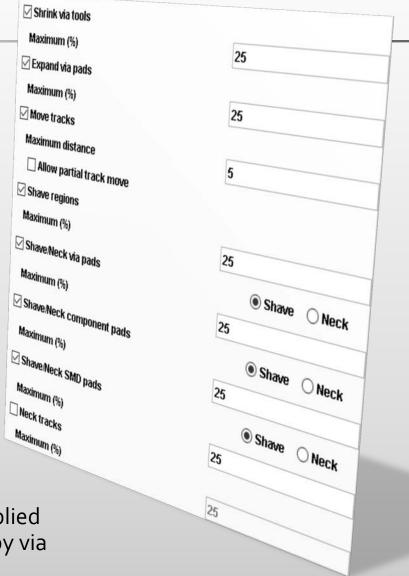




- All-in-one, fully-automatic <u>YELO</u> module
- Adjusts signal layers to meet the fabrication requirements
- Fully automatic
- All parameters entered from a single central user interface
- Parameters storable in parameter sets for easy re-use
- Parameters loadable from existing DRC configuration files
 - ties in seamlessly with the existing CAM workflow

Parameter set Yelo-SAJ		~	
DRC Configuration	1-pad-hole		
Verify Parameters			
Min. pad to pad clearance		0.1	
in. pad to track (excluding regions) clearance		0.223	
in. pad to region clearance		0.223	
Min. track to track (excluding regions) clearance Min. track to region clearance		0.223 0.223	
Min. track to non-plated hole clearance Min. track to outline clearance		0.2032	
Adjust Parameters			
Min. track width		0.08	
Min. pad size		0.2	
Min. hole size		0.0508	
Maximum (%)		25	
🗹 Expand via pads			
Maximum (%)		25	
🗹 Move tracks			
Maximum distance		0.5	
🗹 Allow partial track move			
🗹 Shave regions		25	
☑ Shave regions Maximum (%)			
) Shave	🔘 Neck
Maximum (%)		Shave	() Neck
Maximum (%) 🗹 Shave/Neck via pads	ds	_	_
Maximum (%) 🗹 Shave/Neck via pads Maximum (%)	ds	25	_
Maximum (%) Shave/Neck via pads Maximum (%) Shave/Neck component pa	ds	25 Shave	O Neck
Maximum (%) Shave/Neck via pads Maximum (%) Shave/Neck component pa Maximum (%)	ds	25 Shave	O Neck
Maximum (%) Shave/Neck via pads Maximum (%) Shave/Neck component pa Maximum (%) Shave/Neck SMD pads	ds	25	O Neck
Maximum (%) Shave/Neck via pads Maximum (%) Shave/Neck component pa Maximum (%) Shave/Neck SMD pads Maximum (%)	ds	25	O Neck

- Vast arsenal of adjustment methods to achieve the requested clearances and annular rings
 - Shrink via tools
 - Expand via pads
 - > Move tracks single or multiple full or partial traces
 - Shave regions
 - Shave/Neck via pads
 - Shave/Neck component pads
 - > Shave/Neck SMD pads
 - > Neck tracks
- To suit individual customer requirements
 - > Each adjustment can be enabled or disabled
 - > Each adjustment can be limited in magnitude
- Adjustment precedence. Adjustments lower in the GUI are only applied when absolutely needed to meet spec. E.g. if the spec can be met by via shrinks and track moves alone, regions or pads will not be shaved.

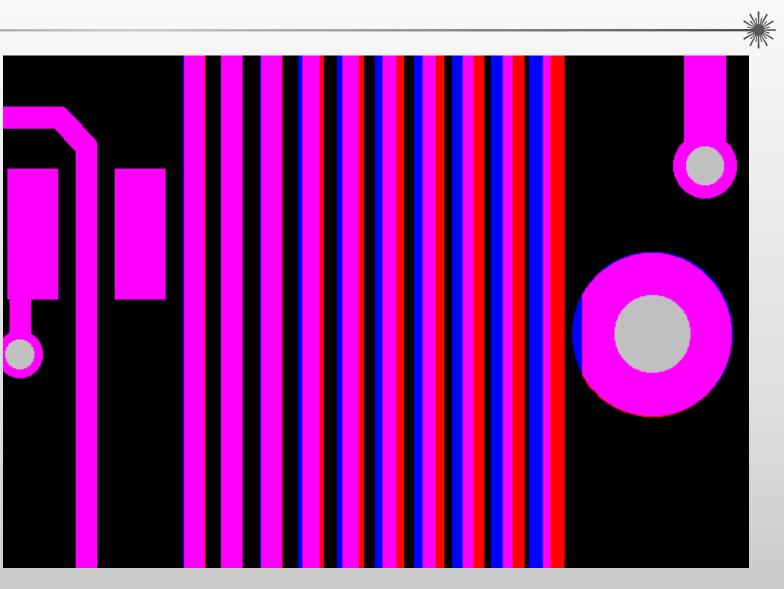


Copper Adjuster (CAJ) - Signal

Precedence Example 1:

A track bundle with internal clearance issues is squeezed between an SMD pad (left) and a via pad (right). There is no room to spread out the tracks without shaving one of the pads.

The bundle is spread towards the right, toward the via pad, because shaving a via pad has precedence over shaving a SMD pad.



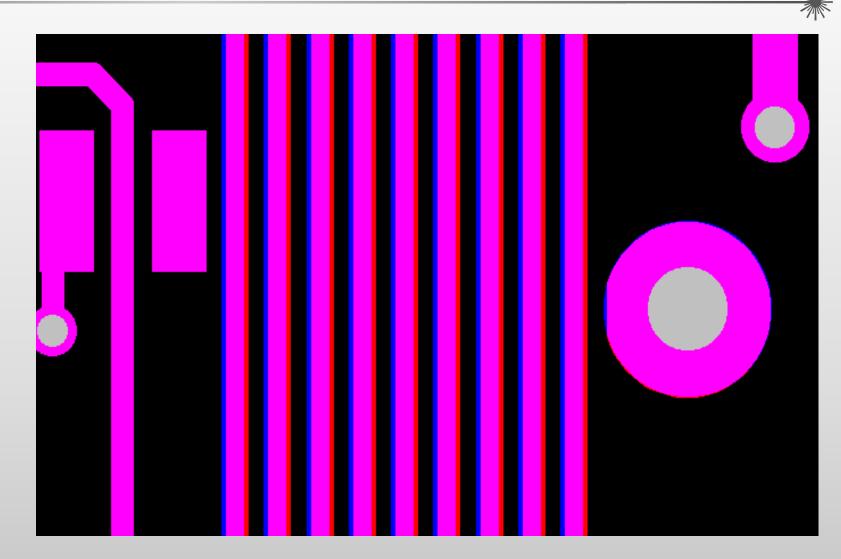


Copper Adjuster (CAJ) - Signal

Precedence Example 2:

Now track bundle has no internal clearance issues, but it is too close to the SMD pad.

Instead of shaving the SMD pad the whole bundle is moved towards the via pad, which is shaved to make room for the bundle.





- Full traceability of all layout changes via
 - Before/After information is added to the DPF data
 - stored persistently in the DPF file
 - easy to consult during the entire CAM process
 - Copper Adjuster Assistant function to:
 - highlight a selection of applied adjustment
 - different colors to display different adjustment types
 - > Automatic back-up of original layers
- Full security thanks to an integrated netlist verification

	<u>a lanne</u>			
Copper Adjuster Assis				
Parameter set Adjustment Type ExpandViaPad MovePartialTrack MoveTrack NeckPad NeckTrack ShavePad ShaveRegion ShrinkViaTool Adjustment Type Exp	Color	Hide	Vumber 0 6 272 0 0 16 16 1 0 v on Selection	
Layer: BS uSAJ_Adju	istment	Set	On Aperture	_

Copper Adjuster (CAJ) - Signal

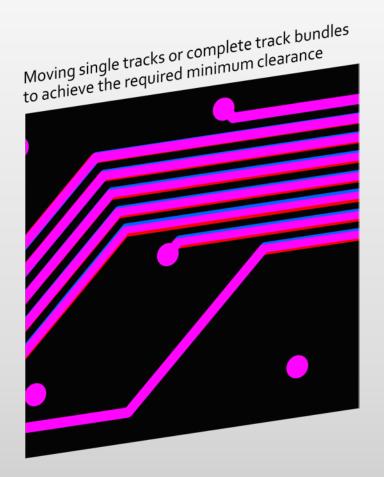


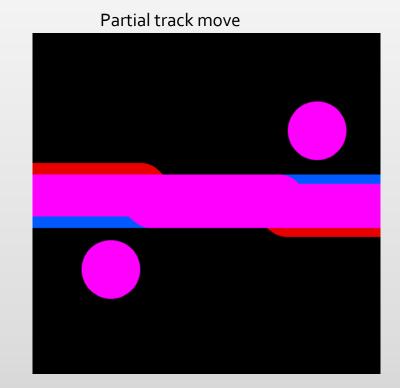
Examples what Copper Adjuster can do for you...





•

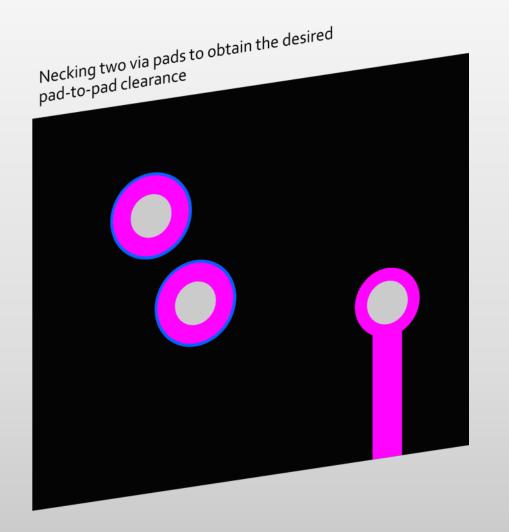


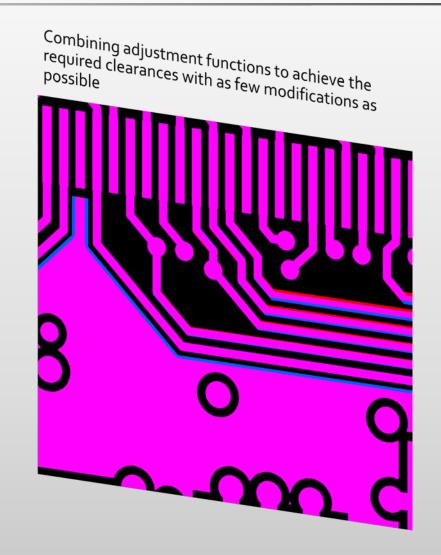




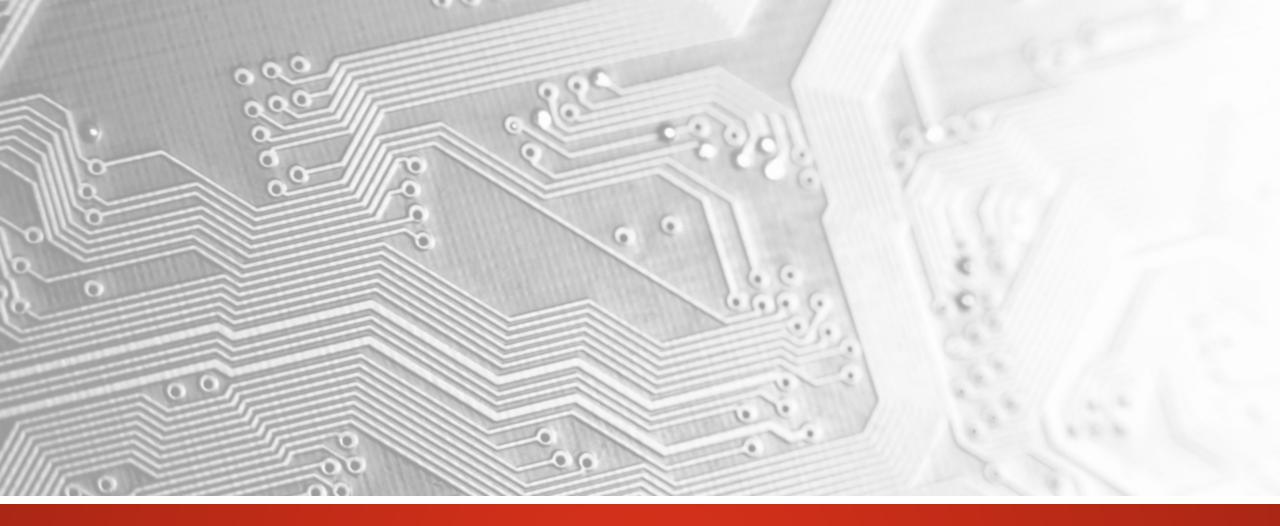












Copper Adjuster - Plane (Beta version)

Copper Adjuster (CAJ) - Plane

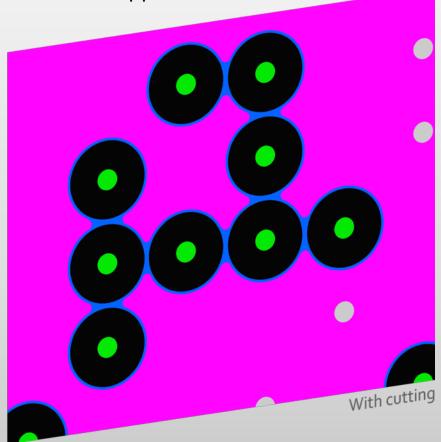
- All-in-one, fully-automatic <u>YELO</u> module
- Adjust plane areas to meet specified fabrication requirements on plane or mixed layers
- Fully automatically
- All parameters entered from a single central user interface
- Parameters storable in parameter sets for easy re-use
- Parameters loadable from existing DRC configuration files
 - ties in seamlessly with the existing CAM workflow

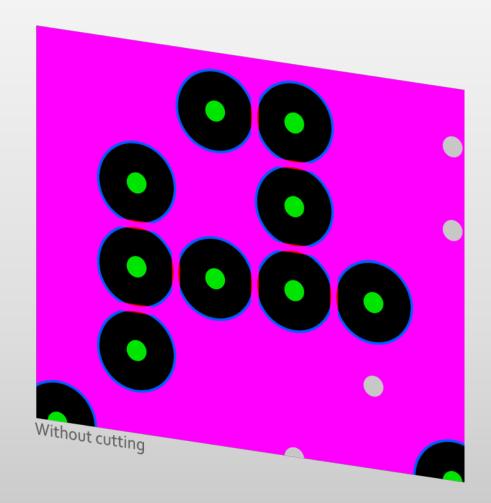
Parameter Set Copper Adjuster - Keep	
DRC configuration Minimum	1
Verify parameters	
Min. plated hole to copper clearance	0.3
Min. non-plated hole to copper clearance	e 0.3
Min. annular ring to copper clearance	0.12
Min. outline to copper clearance	0.3
Min. copper width	0.13
Min. in-plane clearance	0.15
Adjust parameters	
Increase clearances	
Min. annular ring	0.15
Fix copper widths	
Remove bridges None	If netlist allows

Copper Adjuster (CAJ) - Plane (Beta version)

Remove bridges offers the choice to handle too-narrow copper dams by

- cutting the copper dams (with smooth corners)
- widen the copper dams

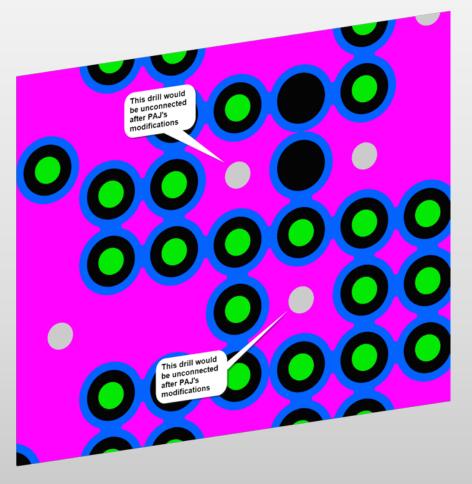


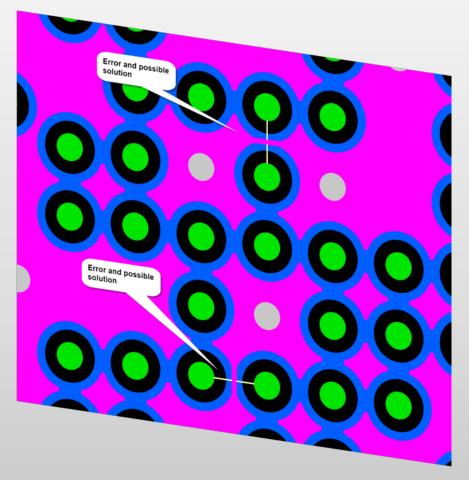




Copper Adjuster (CAJ) - Plane (Beta version)

Where cutting away all copper dams starves pads or drills, Copper Adjuster gives a warning and keeps one dam to maintain netlist integrity. Netlist integrity trumps achieving the drc values.

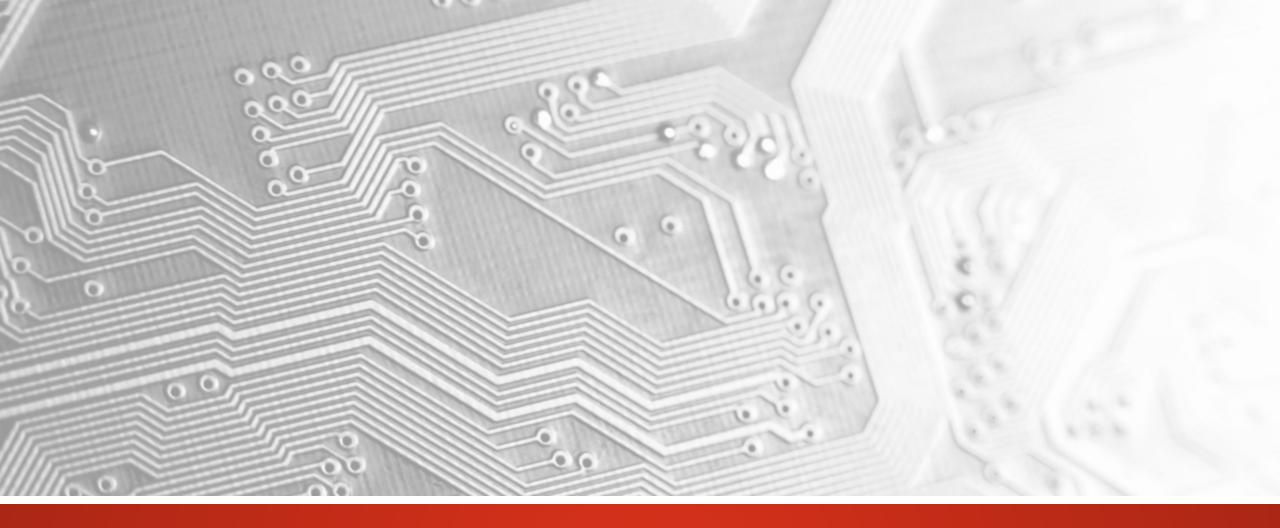




Copper Adjuster (CAJ) - Plane (Beta version)

Where the annular ring and the clearance requirements are in conflict, Copper Adjuster gives a warning and safeguards the ring, sacrificing the clearance. Rings trump clearances.

Error Handling : PAJ Errors Actions Verify Options Errors Loaded:	×
1000 ALL	▼
Grouping Create Groups 0.002 Distance between errors single grouped Scroll	
Current Fault	•
Layer i2 z1 Info Annotations Clear: 0.2844 mm Tool: 3 = CIR,0.25 in mm Track: 995764 = CON in mm	

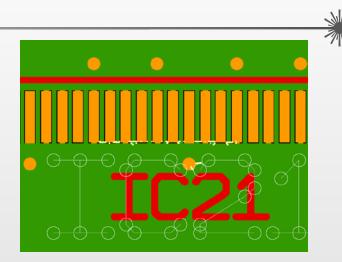


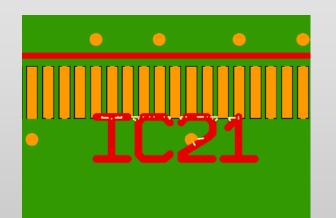
Legend Adjuster (LAJ)

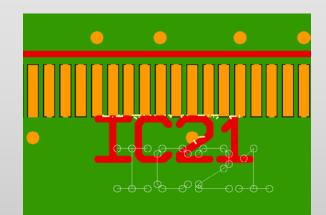
Legend Adjuster (LAJ)

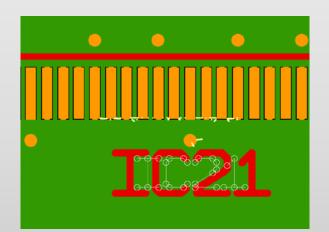
Legend Adjuster

- Dedicated, easy-to-use tools to adjust the size and the position of texts
- Automatic identification of text blocks
- Comprehensive text placement checks



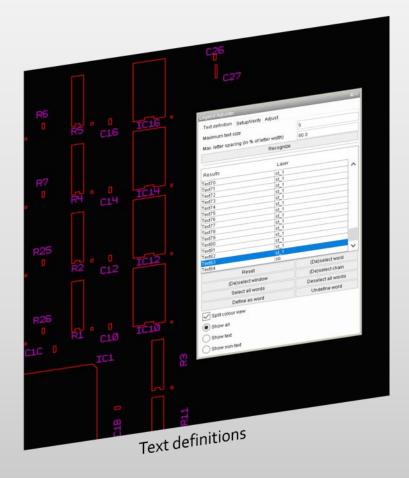






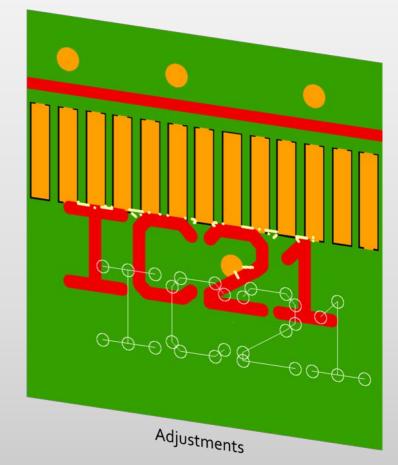
Legend Adjuster (LAJ)

Legend Adjuster allows to move, resize and clip each text block individually, within constraints. It finds text blocks automatically.



Text definition Setup/Verify Ac	djust	
DRC configuration 1-	-pad-hole	~
Min. line width		0.2
Min. line length		0.4
Min. clearance to areas fre	e of mask	0.2
Min. clearance to copper		0
Min. clearance to copper pad		0
Min. clearance to copper free of mask		0
Min. clearance to copper p	ad free of mask	0
Min. clearance to plated dr	rill	0.3
Min. clearance to non-plated drill 0.3		0.3
	Verify	

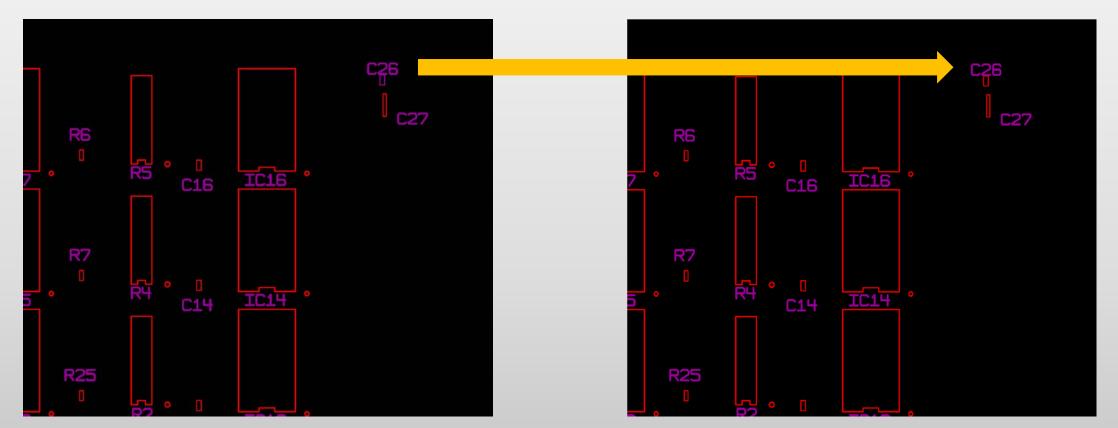
Setup / Verify





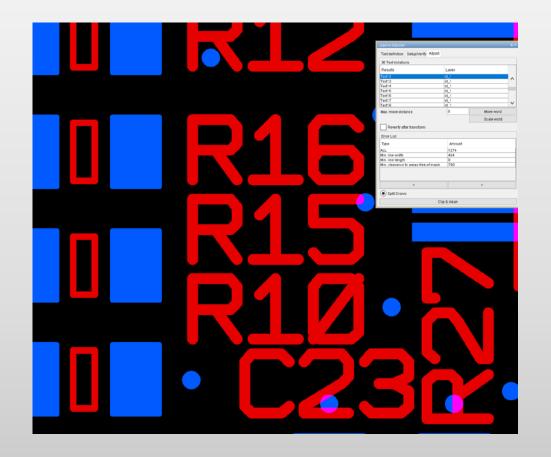
Legend Adjuster (LAJ)

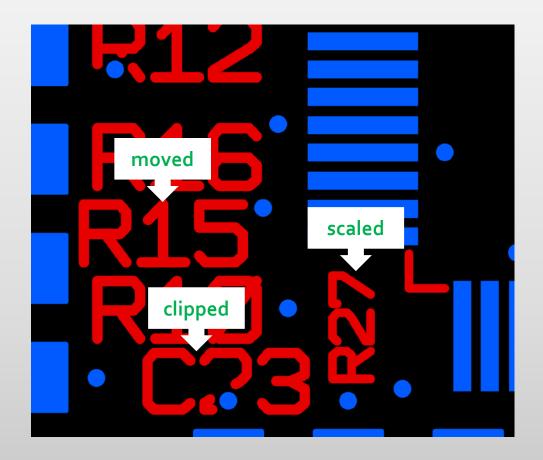
In "Split color view" text defined by LAJ can be highlighted and easily reviewed: Possible misclassifications are easily detected and corrected.



Yelo (Yield Enhancing Layout Optimizer) Legend Adjuster (LAJ)

• All violations are shown and one can quickly decide to *move*, *scale* or *clip* text.

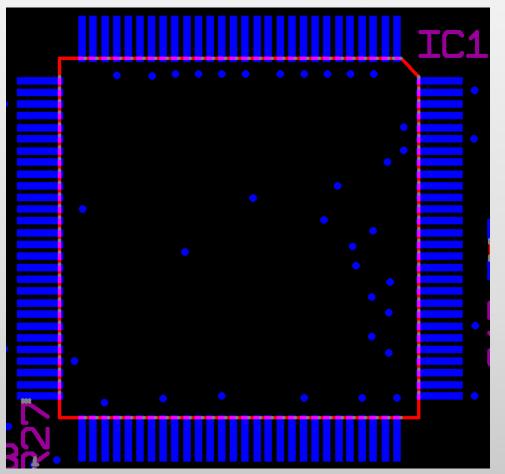


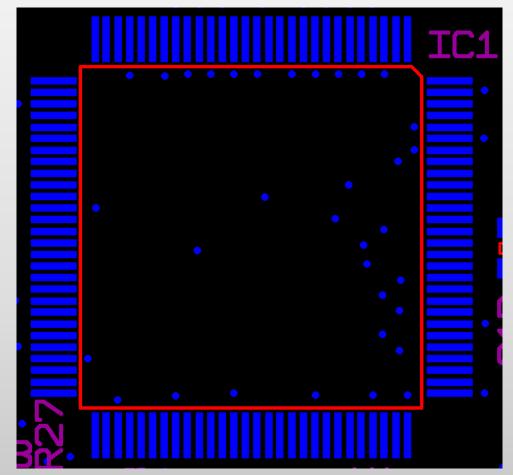




Legend Adjuster (LAJ)

Automatic expansion or shrinkage of component frames.





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