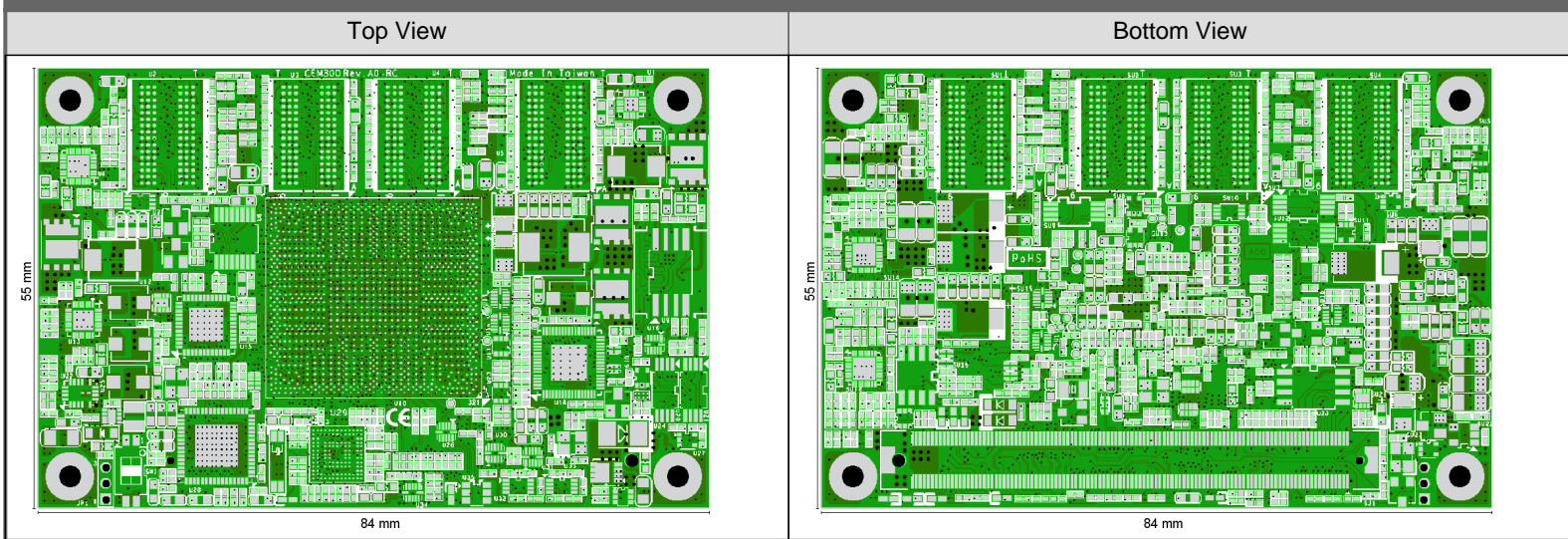


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## Single PCB View - Original



## Summary - General - Original

PCB Size	84 mm x 55 mm	Customer Panel Size	
PCB Thickness	1.962 mm	Max. Aspect Ratio on PTH	9.7
Copper Layers	12	Pressing Stages	3
Surface Finish	HASL	Drill Hole Density	10145 Holes/dm <sup>2</sup>
Solder Mask	Both	Testable Points	6227
Solder Mask Color	Green	Min. SMD/BGA Size	0.2 mm
Legend	Both	Via in Pad	Yes
Legend Color	White	Stacked Vias	Yes
Edge Connector Area	0 dm <sup>2</sup>	Castellated	No
Peeloff Mask	No	Anomalies	Yes
Carbon Mask	No		

## Summary - Copper Layer Minima - Original

Type	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Trace to Trace Clr.	Same Net Clr.	Ring	Copper to Plated Clr.	Copper to NPTH Clr.	Copper to Outline Clr.
	mm	mm	mm	mm	mm	mm	mm	mm	mm	mm	mm
Outer	1 0.040	2 0.089	3 0.089	4 0.089	5 0.102	6 0.102	7 0.001	8 0.102	9 0.137		10 0.203
Inner	11 0.063	12 0.089	13 0.089	14 0.089	15 0.102	16 0.102	17 0.102	18 0.102	19 0.128		20 0.508

## Summary - Sequences - Original

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Routs	Ring on Outer	Ring on Inner	Hole to Copper Clr.	
			mm	mm			mm	mm	mm	
Blind		4	4	0.152	0.152	3043		0.102	0.102	0.203
Buried		2	2	0.254	0.254	684	0		0.102	0.203
PTH		1	8	0.203	4.064	960	0	0.102	0.102	0.128
Plated (Total)		7	14	0.152	4.064	4687	0	0.102	0.102	0.128
Total		7	14	0.152	4.064	4687	0	0.102	0.102	0.128

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### Summary - Rout - Original

Type	Tools	Min. End Dia.	Max. End Dia.	Rout Length
		mm	mm	mm
Plated				
NPTH				
Total				

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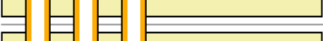



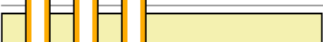




















## Anomalies - Original

Some holes of plated drill tool #8 have no ring at the end of the span in 'drl'  
Some holes of plated drill tool #9 have no ring at the end of the span in 'drl'  
Some holes of plated drill tool #10 have no ring at the end of the span in 'drl'

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Stackup - Original

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		70.0	SILKSCREEN
		50.0	PSR4000 GP01EU
1		17.0	Foil_17_um_Y
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
2		17.0	FOIL_17_uM_H
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
3		17.0	FOIL_17_uM_H
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
4		35.0	Core_0.100_35/35_Hitachi_679_F(J)
5		100.0	
		35.0	
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
6		35.0	Core_0.100_35/35_Hitachi_679_F(J)
7		100.0	
		35.0	
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
8		35.0	Core_0.100_35/35_Hitachi_679_F(J)
9		100.0	
		35.0	
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
10		17.0	FOIL_17_uM_H
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
11		17.0	Foil_17_um_Y
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
		75.0	Prepreg_1080_-_Hitachi_679_F(J)_JUME
12		17.0	Foil_17_um_Y
		50.0	PSR4000 GP01EU
		70.0	SILKSCREEN

ucam-t3/Master

1773.2 um

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Pressing Stages	3
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## Files - Original

Initial	Renamed	Function	Position	Color	Thickness	
					Base	Finished
					mm	mm
PTOP.art	gtp	paste	top			
SKT.art	to	legend	top	white	0.070	0.070
CMK.art	ts	soldermask	top	green	0.050	0.050
TOP.art	tl	outer	1		0.017	0.042
GND.art	l2	inner	2		0.017	0.042
INT1.art	l3	inner	3		0.017	0.042
INT2.art	l4	inner	4		0.035	0.035
GND1.art	l5	inner	5		0.035	0.035
VCC.art	l6	inner	6		0.035	0.035
INT3.art	l7	inner	7		0.035	0.035
GND2.art	l8	inner	8		0.035	0.035
INT4.art	l9	inner	9		0.035	0.035
INT5.art	l10	inner	10		0.017	0.042
GND3.art	l11	inner	11		0.017	0.042
BOTTOM.art	bl	outer	12		0.017	0.042
SMK.art	bs	soldermask	bottom	green	0.050	0.050
SKB.art	bo	legend	bottom	white	0.070	0.070
PBOT.art	gbp	paste	bottom			
CEM300A00BRDRC-1-12.drl	drl	plated	1-12			
CEM300A00BRDRC-1-2.drl	drl_1_2	plated	1-2			
CEM300A00BRDRC-1-3.drl	drl_1_3	plated	1-3			
CEM300A00BRDRC-10-12.drl	drl_10_12	plated	10-12			
CEM300A00BRDRC-11-12.drl	drl_11_12	plated	11-12			
CEM300A00BRDRC-2-11.drl	drl_2_11	plated	2-11			
CEM300A00BRDRC-3-10.drl	drl_3_10	plated	3-10			
DRI_1-12.art	doc	drillmap	none			
DRI_1-2.art	doc1	drillmap	none			
DRI_1-3.art	doc2	drillmap	none			
DRI_10-12.art	doc3	drillmap	none			
DRI_11-12.art	doc4	drillmap	none			
DRI_2-11.art	doc5	drillmap	none			
DRI_3-10.art	doc6	drillmap	none			
profile.dpf	profile	outline	none			

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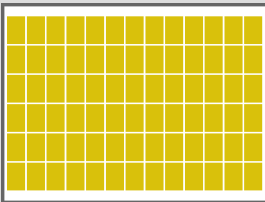
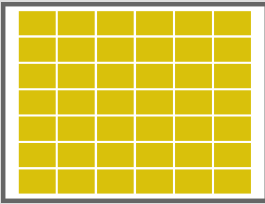
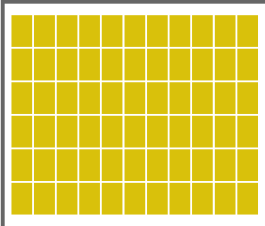
### PCB (Single) - Original

PCB Size	Outline Type	Outline Length	Outline Area	Copper Layers	PCB Thickness
mm x mm		mm	dm <sup>2</sup>		mm
84.000 x 55.000	real	277.999	0.4620	12	1.962

### Customer Panel (Delivery Array, Shipping Panel) - Original

Original Image	Panel Size	Left Border	Right Border	Top Border	Bottom Border	X Spacing	Y Spacing	PCB's	Depanel Rout Length
	mm x mm	mm	mm	mm	mm	mm	mm		mm

### Production Panel (Working Panel) - Original

Panel Name	Panel Size	Useful Area	Drawing	Pieces/Panel	Panel Fill	Panel Usage
	mm x mm	mm x mm			%	%
24x20	610.000 x 508.000	590.000 x 488.000		48	87	72
32x24	812.000 x 610.000	792.000 x 590.000		78	87	73
24x18	610.000 x 457.000	590.000 x 437.000		42	85	70
28x24	711.000 x 610.000	691.000 x 590.000		66	84	70

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### Copper Layer Minima & Area - Original

File	Pos.	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Same Net Clr.	Copper Area	
		mm	mm	mm	mm	mm	mm	dm <sup>2</sup>	%
tl	1	0.040	0.089	0.089	0.089	0.102	0.013	0.2329	50
l2	2	0.102	0.355	>0.400	>0.400	0.102	0.102	0.4145	90
l3	3	0.089	0.089	0.089	0.089	0.102	0.102	0.1431	31
l4	4	0.104	0.114	0.114	0.114	0.102	0.102	0.1481	32
l5	5	0.102	>0.400	>0.400	>0.400	0.102	0.102	0.4161	90
l6	6	0.102	0.342	>0.400	>0.400	0.102	0.127	0.4094	89
l7	7	0.063	0.152	0.152	0.152	0.102	0.102	0.3620	78
l8	8	0.102	>0.400	>0.400	>0.400	0.102	0.102	0.4161	90
l9	9	0.089	0.089	0.089	0.089	0.102	0.102	0.1135	25
l10	10	0.089	0.089	0.089	0.089	0.102	0.102	0.1276	28
l11	11	0.102	0.262	>0.400	>0.400	0.102	0.102	0.4219	91
bl	12	0.089	0.089	0.089	0.089	0.102	0.001	0.2144	46

### Copper Layer Minima - Copper to Drill Minima - Original

File	Pos.	Ring					Copper to Drill Clr.		Copper to Outline Clr.			
		Overall	Via	Laser Via	Comp.	Mech.	Plated	NPTH	Overall	to Pad	to Trace	to Region
		mm	mm	mm	mm	mm	mm	mm	mm	mm	mm	mm
tl	1	0.102	0.102	0.102	0.190	>0.800	0.137		0.203	0.203	0.508	0.508
l2	2	0.102	0.102	0.102	0.190	>0.800	0.128		0.508	0.508	>1.600	0.509
l3	3	0.102	0.102		0.190	>0.800	0.183		0.508	0.508	0.508	0.508
l4	4	0.102	0.102		0.190	>0.800	0.128		0.508	0.508	0.508	0.508
l5	5	0.102	0.102		0.190	>0.800	0.128		0.508	0.508	>1.600	0.509
l6	6	0.102	0.102		0.190	>0.800	0.128		0.508	0.508	>1.600	0.508
l7	7	0.102	0.102		0.190	>0.800	0.128		0.508	0.508	0.517	0.508
l8	8	0.102	0.102		0.190	>0.800	0.128		0.508	0.508	>1.600	0.509
l9	9	0.102	0.102		0.190	>0.800	0.183		0.508	0.508	0.508	>1.600
l10	10	0.102	0.102		0.190	>0.800	0.203		0.508	0.508	0.508	0.561
l11	11	0.102	0.102	0.102	0.190	>0.800	0.128		0.508	0.508	>1.600	0.509
bl	12	0.102	0.102	0.102	0.190	>0.800	0.203		0.223	0.223	0.517	0.508



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### Copper Layers - Copper to Copper Clearances - Original

File	Pos.	Copper to Copper Clr.				
		Overall	Pad to Pad	Pad to Track	Track to Track	Trace to Trace
		mm	mm	mm	mm	mm
tl	1	0.102	0.102	0.102	0.102	0.102
l2	2	0.102	0.102	0.127	>0.500	>0.500
l3	3	0.102	0.102	0.102	0.102	0.102
l4	4	0.102	0.102	0.102	0.102	0.102
l5	5	0.102	0.102	0.127	>0.500	>0.500
l6	6	0.102	0.102	0.127	0.203	>0.500
l7	7	0.102	0.102	0.102	0.127	0.127
l8	8	0.102	0.102	0.127	>0.500	>0.500
l9	9	0.102	0.102	0.102	0.102	0.102
l10	10	0.102	0.102	0.102	0.102	0.102
l11	11	0.102	0.102	0.127	>0.500	>0.500
bl	12	0.102	0.102	0.102	0.102	0.102

### Copper Areas - Original

Side	Total	Free of				Edge Connectors		
		Solder Mask (as supplied)	Solder Mask (open vias)	Gold Mask	Silver Mask	Fingers	Finger Size	Total Area
	dm <sup>2</sup>	dm <sup>2</sup>	dm <sup>2</sup>	dm <sup>2</sup>	dm <sup>2</sup>		mm x mm	dm <sup>2</sup>
Top (incl. 1/2 plated holes and routs)	0.3274	0.1238	0.2199					
Bottom (incl. 1/2 plated holes and routs)	0.2982	0.1258	0.2111					
Total (incl. plated holes and routs)	0.6256	0.2495	0.4309			0		

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## Drill Tools - Original

File	Tool Nr.	Span	Type	Function	Method	Filled Via	Counter	Dia.	Tol. -	Tol. +	Holes in PCB	Routs in PCB	Double Hits	Predrill Hits
								mm	mm	mm				
drl	2	1-12	PTH	via	mech.	none	unknown	0.203	0.000	0.000	719	0	0	0
drl	4	1-12	PTH	via	mech.	none	unknown	0.254	0.000	0.000	110	0	0	0
drl	5	1-12	PTH	via	mech.	none	unknown	0.406	0.000	0.000	118	0	0	0
drl	6	1-12	PTH	comp.	mech.	none	unknown	0.889	0.000	0.000	3	0	0	0
drl	7	1-12	PTH	comp.	mech.	none	unknown	2.718	0.000	0.000	4	0	0	0
drl	8	1-12	PTH	mech.	mech.	none	unknown	0.914	0.000	0.000	1	0	0	0
drl	9	1-12	PTH	mech.	mech.	none	unknown	1.600	0.000	0.000	1	0	0	0
drl	10	1-12	PTH	mech.	mech.	none	unknown	4.064	0.000	0.000	4	0	0	0
drl_10_12	1	10-12	Blind	via	mech.	none	unknown	0.152	0.000	0.000	474	0	0	0
drl_11_12	1	11-12	Blind	via	laser	none	unknown	0.152	0.000	0.000	717	0	0	0
drl_1_2	1	1-2	Blind	via	laser	none	unknown	0.152	0.000	0.000	1012	0	0	0
drl_1_3	1	1-3	Blind	via	mech.	none	unknown	0.152	0.000	0.000	840	0	0	0
drl_2_11	3	2-11	Buried	via	mech.	none	unknown	0.254	0.000	0.000	43	0	0	0
drl_3_10	3	3-10	Buried	via	mech.	none	unknown	0.254	0.000	0.000	641	0	0	0

## Drill Tools - Drill vs Copper - Original

File	Tool Nr.	Span	Type	Function	Method	Dia.	Ring on Outer	Ring on Inner	Min. Pad Size
						mm	mm	mm	mm
drl	2	1-12	PTH	via	mech.	0.203	0.102	0.102	0.407
drl	4	1-12	PTH	via	mech.	0.254	0.127	0.127	0.508
drl	5	1-12	PTH	via	mech.	0.406	0.178	0.178	0.762
drl	6	1-12	PTH	comp.	mech.	0.889	0.190	0.190	1.269
drl	7	1-12	PTH	comp.	mech.	2.718	>0.800	0.508	3.734
drl	8	1-12	PTH	mech.	mech.	0.914	>0.800	>0.800	> 2.514
drl	9	1-12	PTH	mech.	mech.	1.600	>0.800	>0.800	> 3.200
drl	10	1-12	PTH	mech.	mech.	4.064	>0.800	>0.800	> 5.664
drl_10_12	1	10-12	Blind	via	mech.	0.152	0.102	0.102	0.356
drl_11_12	1	11-12	Blind	via	laser	0.152	0.102	0.102	0.356
drl_1_2	1	1-2	Blind	via	laser	0.152	0.102	0.102	0.356
drl_1_3	1	1-3	Blind	via	mech.	0.152	0.102	0.102	0.356
drl_2_11	3	2-11	Buried	via	mech.	0.254	>0.800	0.102	0.458
drl_3_10	3	3-10	Buried	via	mech.	0.254	>0.800	0.102	0.458

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### Sequences - Original

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Ring on Outer	Ring on Inner	Hole to Copper Clr.	Hole to Hole Clr., within Seq.	Overlapping Holes, within Seq.	Hole to Hole Clr., between Seqs	Hole to Outline Clr.	Slot to Outline Clr.
			mm	mm		mm	mm	mm	mm		mm	mm	mm
1-12	PTH	8	0.203	4.064	960	0.102	0.102	0.128	0.305	No	0.305	0.000	>6.400
1-3	Blind	1	0.152	0.152	840	0.102	0.102	0.203	0.305	No	0.306	0.610	>6.400
1-2	Blind	1	0.152	0.152	1012	0.102	0.102	0.203	0.305	No	>0.800	0.610	>6.400
2-11	Buried	1	0.254	0.254	43		0.102	0.203	0.587	No	0.330	3.175	>6.400
3-10	Buried	1	0.254	0.254	641		0.102	0.203	0.305	No	>0.800	0.610	>6.400
10-12	Blind	1	0.152	0.152	474	0.102	0.102	0.203	0.305	No	0.305	0.610	>6.400
11-12	Blind	1	0.152	0.152	717	0.102	0.102	0.203	0.306	No	>0.800	0.686	>6.400
All	Plated	14	0.152	4.064	4687	0.102	0.102	0.128	0.305	No	0.305	0.000	>6.400
All	All	14	0.152	4.064	4687	0.102	0.102	0.128	0.305	No	0.305	0.000	>6.400

### Rout Tools - Original

File	Tool Nr.	Type	Tool Dia.	End Dia.	Rout Length	Nibble Count
			mm	mm	mm	

### Routed Holes - Original

File	Hole Nr.	Instances	X Size	Y Size	Rout Length	Nibble Count
			mm	mm	mm	

### Sequences Analysis - Original

File	Pos.	Stacked Vias	Overlapped Vias	Via Plug Clr.	Top Tool			Bottom Tool		
					Top Drill File	Tool Nr.	Dia.	Bottom Drill File	Tool Nr.	Dia.
				mm			mm			mm
I2	2	No	4	0.022	drl_2_11	3	0.254	drl_1_2	1	0.152
I3	3	No	2	0.305	drl_3_10	3	0.254	drl_1_3	1	0.152
I10	10	No	1	0.305	drl_10_12	1	0.152	drl_3_10	3	0.254
I11	11	Yes	0	0.000	drl_11_12			drl_2_11		

### SMD (Incl. BGA) - Original

Side	SMD (Incl. BGA)				SMD (Excl. BGA)	BGA				
	Pads	Min. Pad	Pitch of Min. Pad	Solder Mask Defined Pads	Pads	Pads	Min. Pad	Min. Pitch	All Tracks Centered	Drilled Pads
		mm	mm				mm	mm		
Top	3087	0.200	0.400	9	2575	512	0.254	0.500	No	Yes
Bottom	2121	0.200	0.400	0	1737	384	0.356	0.800	No	No
Both	5208	0.200	0.400	9	4312	896	0.254	0.500	No	Yes

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### Solder Mask - Original

Side	Mask to Mask Clr.	Web	Ring on Cu Defined Pads	Ring on SM Defined Pads	Mask to Copper Clr.	Mask Opening	Fully Covered Via Holes	Partly Covered Via Holes	One Side Covered Vias	Both Sides Covered Vias	No Side Covered Vias
	mm	mm	mm	mm	mm	mm					
Top	0.064	0.038	0.013	0.057	0.006	0.254	Yes	Yes			
Bottom	0.084	0.038	0.013	>0.250	0.000	0.300	Yes	Yes			
Both	0.064	0.038	0.013	0.057	0.000	0.254	Yes	Yes	Yes	Yes	No

### Carbon Masks - Original

File	Position	Carbon Width	Carbon to Carbon Clr.	Clr. to Plated Hole	Clr. to Outline	Layer Area	
		mm	mm	mm	mm	dm <sup>2</sup>	%

### Peeloff Masks - Original

File	Position	Min. Peelable Width	Peelable to Peelable Clr.	Clr. to Plated Hole	Clr. to Outline	Layer Area	
		mm	mm	mm	mm	dm <sup>2</sup>	%

### Legend Layers - Original

File	Position	Legend Width	Legend to Legend Clr.	Legend to (Comp/SMD/BGA) Pad Clr.	Layer Area	
		mm	mm	mm	dm <sup>2</sup>	%
to	top	0.100	0.005	0.000	0.0594	13
bo	bottom	0.100	0.001	0.000	0.0747	16

### Gold Layers - Original

File	Position	Gold to Gold Clr.	Clr. to Outline	Layer Area	
		mm	mm	dm <sup>2</sup>	%

### Heatsink Layers - Original

File	Position	Heatsink to Heatsink Clr.	Clr. to Outline	Layer Area	
		mm	mm	dm <sup>2</sup>	%

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### Scoring - Minimum Clearance - Original

File	Pos.	Copper to Score Top	Copper to Score Right	Copper to Score Bottom	Copper to Score Left
		mm	mm	mm	mm
tl	1	0.517	0.510	0.203	0.495
l2	2	0.509	0.509	0.508	0.508
l3	3	0.517	0.510	0.508	0.508
l4	4	0.517	0.510	0.508	0.508
l5	5	0.509	0.509	0.508	0.508
l6	6	0.508	0.508	0.508	0.508
l7	7	0.517	0.508	0.508	0.508
l8	8	0.509	0.509	0.508	0.508
l9	9	0.517	0.510	0.508	0.508
l10	10	0.517	0.510	0.508	0.508
l11	11	0.509	0.509	0.508	0.508
bl	12	0.517	0.408	0.223	0.444

### Scoring - Routing (based on outline analysis) - Original

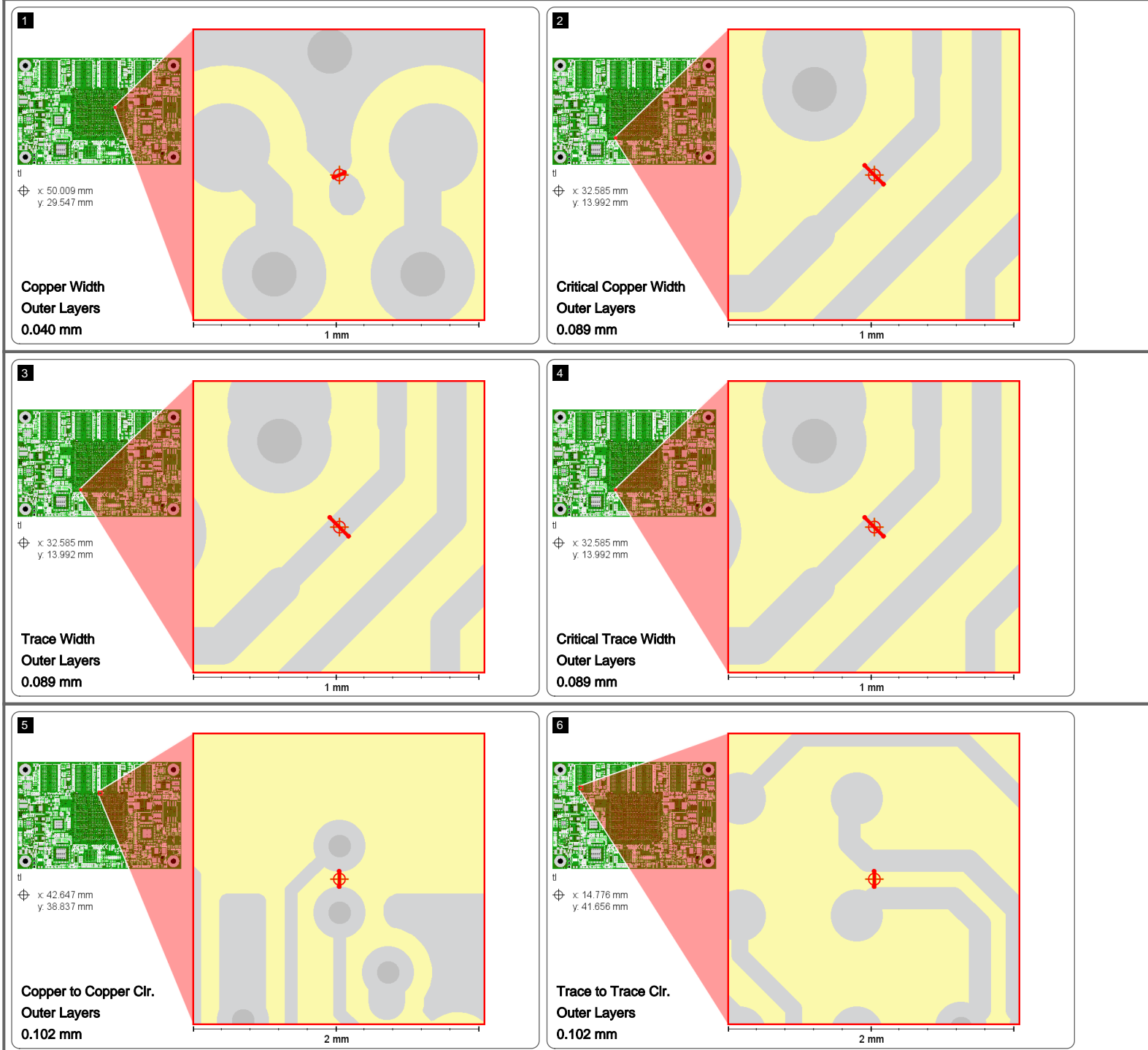
Side	Lines	Score Lines Clr.	Saved Routing	Remaining Routing
		mm	mm	mm
<b>Horizontal Score Lines</b>				
Top	1	0.508	>1.600	
Mid	0			
Bottom	1	0.203	>1.600	
<b>Vertical Score Lines</b>				
Left	1	0.444	>1.600	
Mid	0			
Right	1	0.408	>1.600	
<b>All Score Lines</b>				
	4	0.203	278.000	0.000

### Bare Board Test - Original

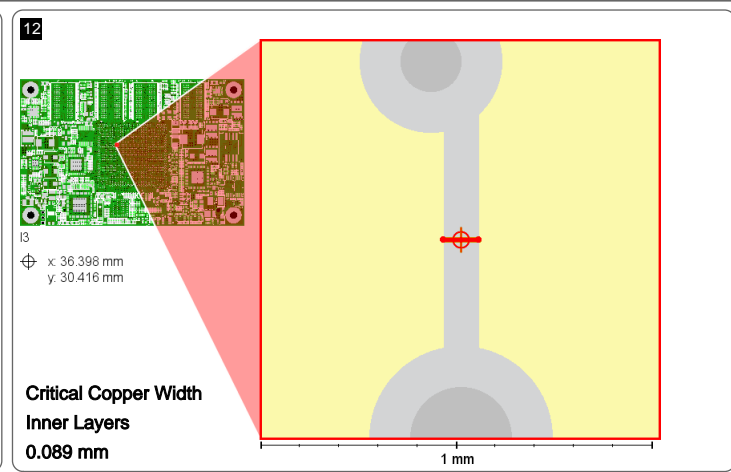
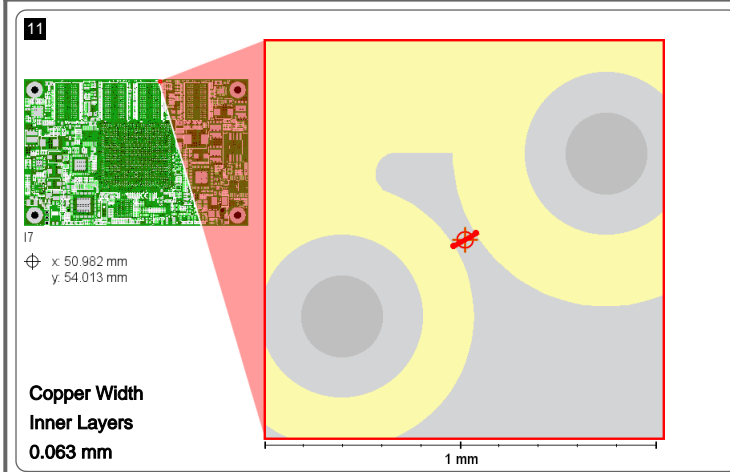
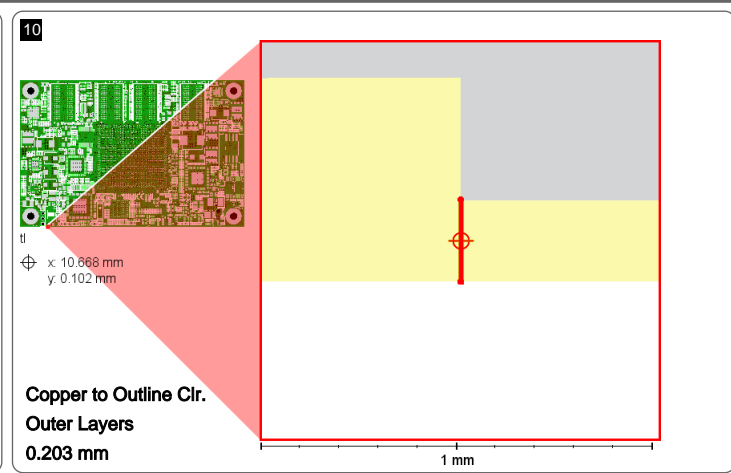
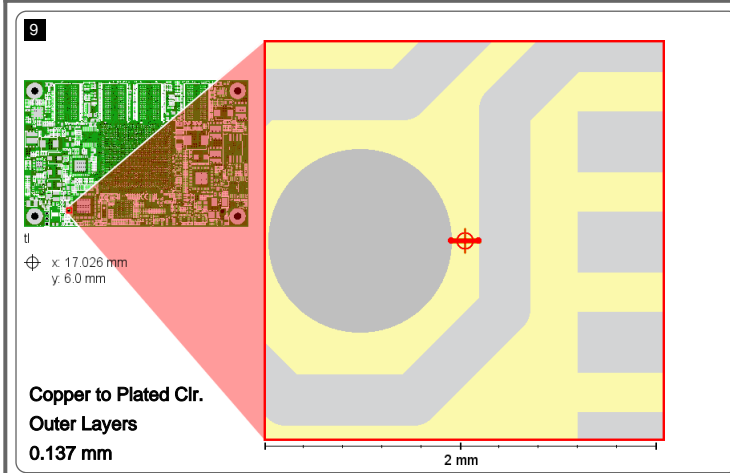
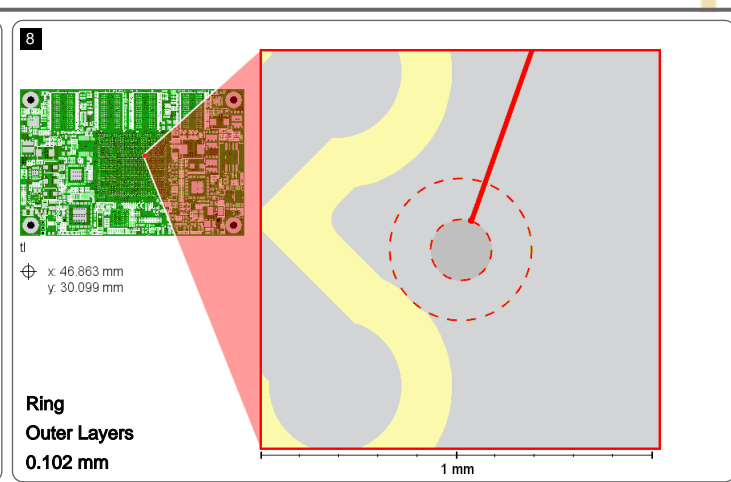
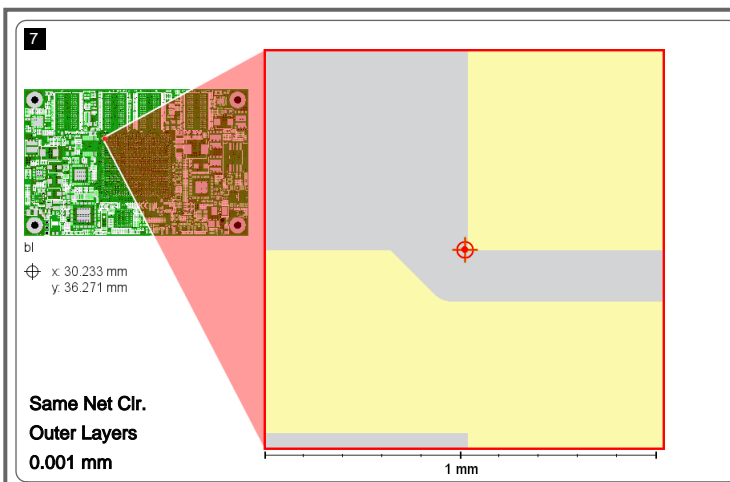
Side	Testable Points (TPs)	Max. TP Density	SMD Pads	Min. SMD Pad	Pitch of Min. SMD Pad	Edge Connector Fingers	Number of Nets
		TP/dm <sup>2</sup>		mm	mm		
Top	3554	14055	3087	0.200	0.400	0	
Bottom	2673	7288	2121	0.200	0.400	0	
Both	6227	14055	5208	0.200	0.400	0	1311

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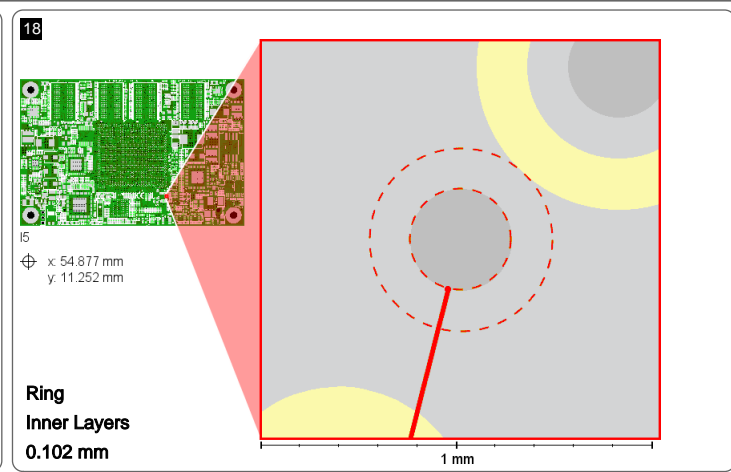
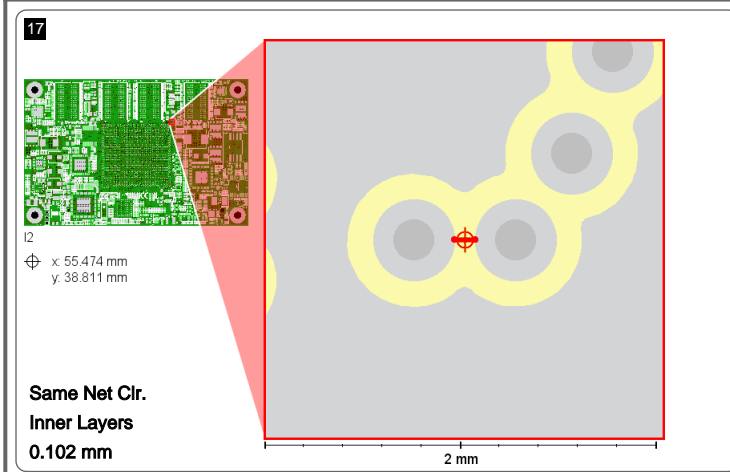
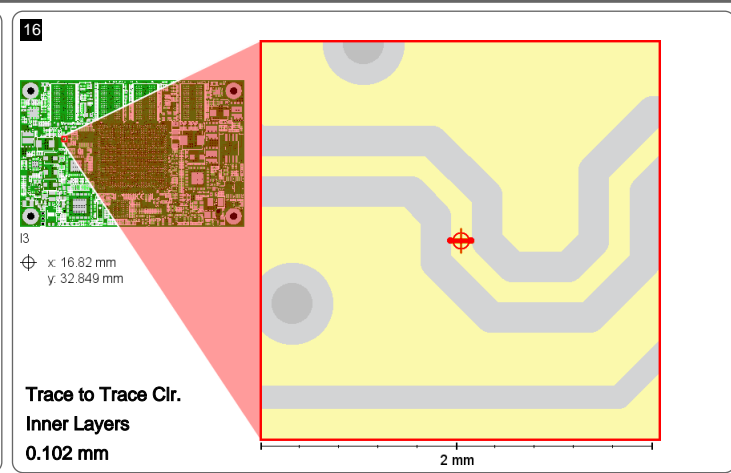
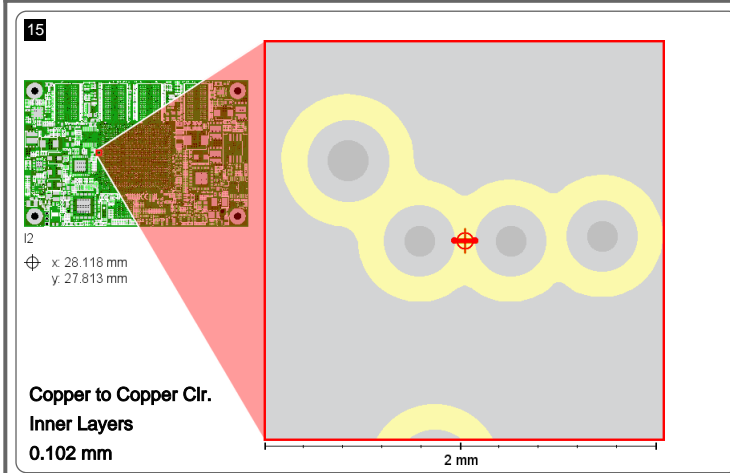
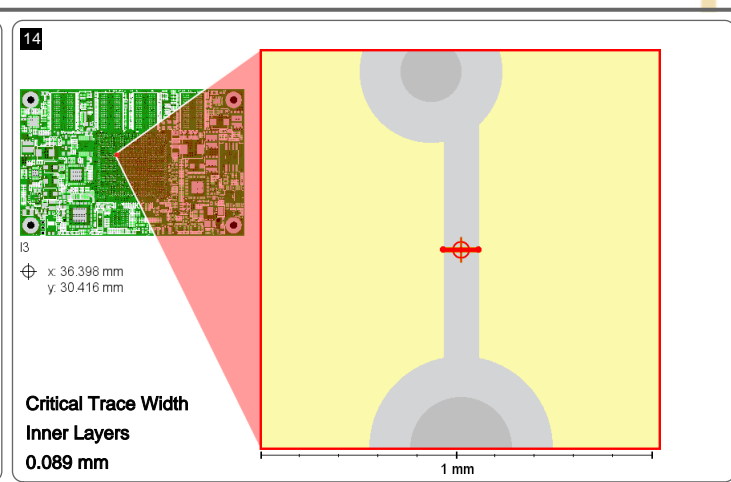
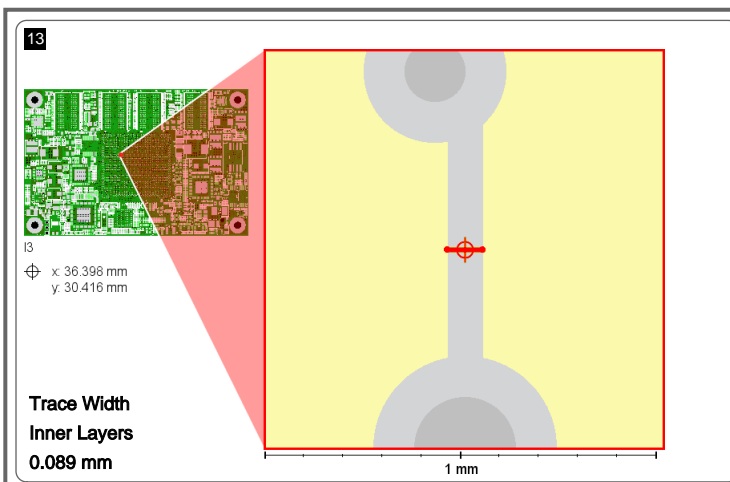
## Summary Minimum Design Characteristics - Locations - Original



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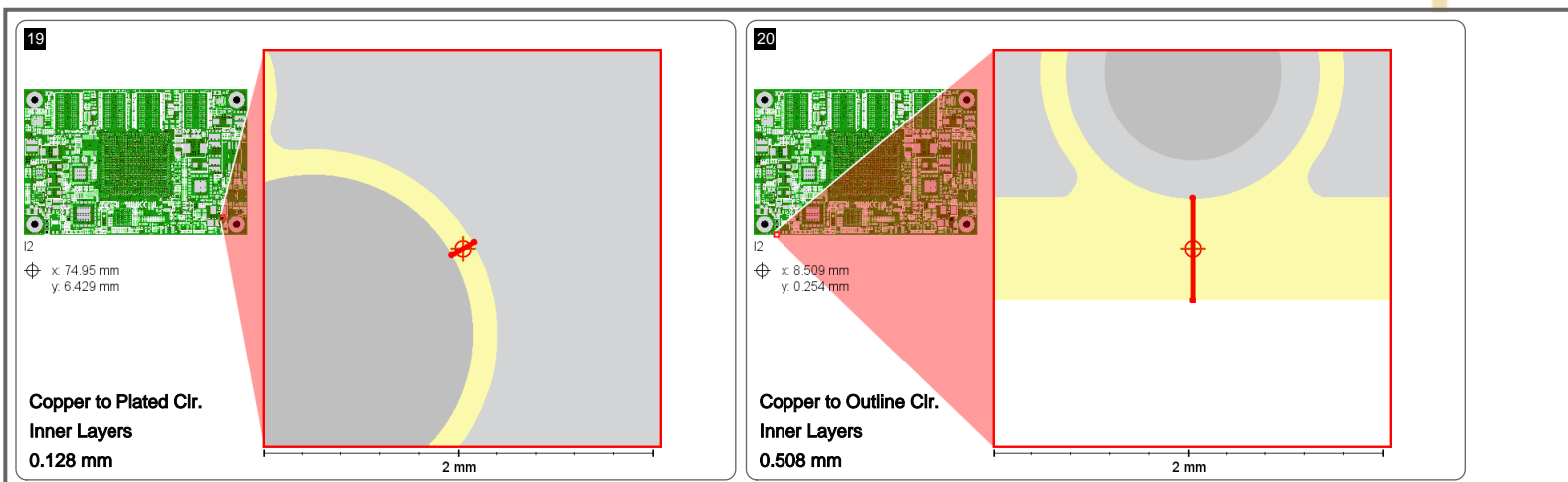


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### Process Parameters - Original

Used units			
Copper Thickness Unit	um	All Other Units	mm

### Process Parameters - Original

Technology			
PCB Finish	HAL	Material Type	FR4 TG 150
CU Base Thickness Outer	17	CU Base Thickness Inner	35
Edge Plating	No	Viahole Filling	Via's covered Both
Logo	UL	Date Code	Month-Year
Impedance	Yes	Drill tolerance	+/- 0.10mm

### Customer and Job Identification - Original

Customer			
Customer	Ucamco NV	Contact Person	John Doe
Email	john.doe@gmail.com	Code gamme	

### Customer and Job Identification - Original

Job			
Article Id	Demo QED report	Board Id	Demo QED report
Redmine Id	N/A	IPC Class for AR	

### Comments - Original

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